

IN THE SPECIFICATION:

Please amend the first paragraph on page 12 as follows:

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In the preferred embodiment of the present invention, both the first scanning control signal or the second scanning control signal generated by driving circuit 110 comprise the 10 signal lines described below: (1)VCC (power source); (2)GND (ground); (3)VGH (the high level of the scanning signal); (4)VGL (the low level of the scanning signal); (5)R/L (representing the data shifting direction to be right, denoted by "R", or left, denoted by "L"); (6)STVR (forward scanning signal); (7)STVL (backward scanning signal); (8)CKV (vertical clock pulse); (9)OE (output enabling signal); and (10)VCOM (common electrode voltage). Referring to FIG. 5 and FIG. 6, the ten signal lines are allocated in the numbering order from the starting position "•." It is noticed that the signal line for the forward scanning signal STVR traces toward the right side of the scanning circuit board 160 (162), the signal line for the backward scanning signal STVL traces toward the left side of the scanning circuit board 160 (162), the signal lines for the other signals VCC, GND, VGH, VGL R/L, CKV OE and VCOM traces toward both the left and right side of the scanning circuit board 160(162), and scan driver ICs 140a~140e and scan driver ICs 150a~150e are allocated in the reverse directions.
